

FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Attorney Docket Number

51346/DBP/B600

Application Number

10/694,282

Filing Date

October 27, 2003

Applicant(s)

Ian S. Eslick, et al.

Group Art Unit

2183

Examiner Name

Thomas C. Lee

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE


FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
CL		BREBNER, Gordon, "A Virtual Hardware Operating System for the Xilinx XC6200", Field-Programmable Logic and Applications, 6th International Workshop, September 23-25, 1996, pp. 327-336, XP008037568
EL		JEAN, Jack S.N., et al., "Dynamic Reconfiguration to Support Concurrent Applications", IEEE Transactions on Computers, Vol. 48, No. 6, June 1999, pp. 591-602, XP-000832606
EL		LEE, Ming-Hau, et al., "Design and Implementation of the MorphoSys Reconfigurable Computing Processor", Journal of VLSI Signal Processing Systems, March 2000, pp. 147-162, Kluwer Academic Publishers, The Netherlands, XP-000908462
EL		European Search Report for European Patent Application No. EP 01 25 0247 dated December 7, 2004, 2pp.

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Sheet 1 of 1

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U.S. PATENT DOCUMENTS				
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OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
EC		"Smart Compilers Puncture Code Bloat," Brown, Electronic Engineering Times, October 9, 1995 (pages 38 & 42)
EC		"A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Razdan et al., Micro-27 Proceedings of the 27th Annual International Symposium on Microarchitecture, 11/30 - 12/2/94 (pp. 172-180).
EC		"Programmable Active Memories: Reconfigurable Systems Come of Age," IEEE Transactions on VLSI Systems, 1995 (pp. 1-15).
EC		"Pilkington Preps Reconfigurable Video DSP," Clark, EE Times, week of July 31, 1995

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EC		"SOP: Adaptive Massively Parallel System," by Tsukasa Yamauchi et al., NEC Research & Development, Vol. 37, No. 3, July 1996 (pp. 382-393).

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